



PATENT APPLICATION
Q-51885

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Akitaka KIMURA

Appln. No.: 37 C.F.R. §1.53(d) Continuation
of 09/161,981, filed September 29, 1998

Group Art Unit: 2812

Confirmation No.: 8269

Filed: September 29, 1999

Examiner: Mulpuri, S.

For: SEMICONDUCTOR LAYER FORMED BY SELECTIVE DEPOSITION AND
METHOD FOR DEPOSITING SEMICONDUCTOR LAYER

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Preliminary to examination of the above-identified Application, please make the
following amendments:

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APR 23 2003
TC 2800 MAIL ROOM

#241/2
Original